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L15	L14 and (asic or application near3 specific)	156	L15

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L12	(713/501)[CCLS]	730	L12
L11	(710/266)![CCLS]	265	L11
L10	(710/266)[CCLS]	265	L10
L9	(713/401-601)[CCLS]	3671	L9
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L5	(asic or application near3 specific) and l2	16917	L5
L4	(asic or application near3 specific) and l1	652	L4
L3	interrupt$6 near18 return$5 and (extension$1 or extend$)	14301	L3
L2	(delay$5 or wait$4) near6 (cycle$1 or clock$3 or period$1)	196651	L2
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1. **A "double-face" bit-serial architecture for the 1D discrete wavelet transform**
 Marino, F.;
 Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circ
 Express Briefs, IEEE Transactions on]
 Volume 47, Issue 1, Jan. 2000 Page(s):65 - 71
 Digital Object Identifier 10.1109/82.818896
 AbstractPlus | References | Full Text: PDF(324 KB) IEEE JNL
 Rights and Permissions

2. **Continuous monitoring of a capacitor bank**
 Mielke, A.M.; Boyle, C.M.; Buenafe, C.A.; Dreicer, J.S.; Gattiker, J.R.; Martinez, B.J.; Smith, D.A.;
 Instrumentation & Measurement Magazine, IEEE
 Volume 4, Issue 4, Dec. 2001 Page(s):14 - 19
 Digital Object Identifier 10.1109/5289.975460
 AbstractPlus | References | Full Text: PDF(5075 KB) IEEE JNL
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3. **Single-wafer/mini-batch approach for fast cycle time in advanced 300-mm fab**
 Bonnin, O.; Mercier, D.; Levy, D.; Henry, M.; Pouilloux, I.; Mastromatteo, E.;
 Semiconductor Manufacturing, IEEE Transactions on
 Volume 16, Issue 2, May 2003 Page(s):111 - 120
 Digital Object Identifier 10.1109/TSM.2003.810920
 AbstractPlus | References | Full Text: PDF(2423 KB) IEEE JNL
 Rights and Permissions

4. **Propagation impairment on Ka-band satcom links in tropical and equatorial regions**
 Green, H.E.;
 Antennas and Propagation Magazine, IEEE
 Volume 46, Issue 2, April 2004 Page(s):31 - 45
 Digital Object Identifier 10.1109/MAP.2004.1305532
 AbstractPlus | Full Text: PDF(1203 KB) IEEE JNL
 Rights and Permissions

5. **Sequential random coding error exponents for multiple access channels**
 Cheng Chang; Sahai, A.;
 Wireless Networks, Communications and Mobile Computing, 2005 International Conference on
 Volume 2, 13-16 June 2005 Page(s):1581 - 1586 vol.2
 Digital Object Identifier 10.1109/WIRLES.2005.1549649

AbstractPlus | Full Text: PDF(1816 KB) IEEE CNF
Rights and Permissions

6. **Coverage-adaptive random sensor selection with latency control for application-specific dat wireless sensor networks**
Das, S.K.; Wook Choi;
Control and Automation, 2005. ICCA '05. International Conference on
Volume 1, 26-29 June 2005 Page(s):214 - 219 Vol. 1
Digital Object Identifier 10.1109/ICCA.2005.1528119
AbstractPlus | Full Text: PDF(4368 KB) IEEE CNF
Rights and Permissions

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Young, C.; Lakshman, Y.N.; Szymanski, T.; Reppy, J.; Presotto, D.; Pike, R.; Narlikar, G.; Mullende
Hot Topics in Operating Systems, 2001. Proceedings of the Eighth Workshop on
20-22 May 2001 Page(s):47 - 52
AbstractPlus | Full Text: PDF(765 KB) IEEE CNF
Rights and Permissions

8. **Implementing flexible object group invocation in networked systems**
Morgan, G.; Shrivastava, S.K.;
Dependable Systems and Networks, 2000. DSN 2000. Proceedings International Conference on
25-28 June 2000 Page(s):439 - 448
Digital Object Identifier 10.1109/ICDSN.2000.857573
AbstractPlus | Full Text: PDF(740 KB) IEEE CNF
Rights and Permissions

9. **Towards more powerful and flexible synchronization primitives**
Borkowski, J.;
Parallel Computing in Electrical Engineering, 2000. PARELEC 2000. Proceedings. International Co
27-30 Aug. 2000 Page(s):18 - 22
Digital Object Identifier 10.1109/PCEE.2000.873594
AbstractPlus | Full Text: PDF(364 KB) IEEE CNF
Rights and Permissions

10. **System-level verification-a comparison of approaches**
Turner, R.;
Rapid System Prototyping, 1999. IEEE International Workshop on
16-18 June 1999 Page(s):154 - 159
Digital Object Identifier 10.1109/IWRSP.1999.779046
AbstractPlus | Full Text: PDF(132 KB) IEEE CNF
Rights and Permissions

11. **Improving the safety of AUVs**
Ortiz, A.; Proenza, J.; Bernat, G.; Oliver, G.;
OCEANS '99 MTS/IEEE. Riding the Crest into the 21st Century
Volume 2, 13-16 Sept. 1999 Page(s):979 - 984 vol.2
Digital Object Identifier 10.1109/OCEANS.1999.805005
AbstractPlus | Full Text: PDF(424 KB) IEEE CNF
Rights and Permissions

12. **Rapid prototyping environment for design of hardware/software electronic systems**
Abid, M.;
Electronics, Circuits and Systems, 1998 IEEE International Conference on
Volume 1, 7-10 Sept. 1998 Page(s):531 - 535 vol.1
Digital Object Identifier 10.1109/ICECS.1998.813378
AbstractPlus | Full Text: PDF(344 KB) IEEE CNF
Rights and Permissions

13. **Implementing C designs in hardware: a full-featured ANSI C to RTL Verilog compiler in actio**
Soderman, D.; Panchul, Y.;
Verilog HDL Conference and VHDL International Users Forum, 1998. IVC/VIUF. Proceedings., 199
16-19 March 1998 Page(s):22 - 29
Digital Object Identifier 10.1109/IVC.1998.660676
AbstractPlus | Full Text: PDF(600 KB) IEEE CNF
Rights and Permissions

14. **Scheduling in co-partitioned array architectures**
Eckhardt, U.; Merker, R.;
Application-Specific Systems, Architectures and Processors, 1997. Proceedings., IEEE Internationi
14-16 July 1997 Page(s):219 - 228
Digital Object Identifier 10.1109/ASAP.1997.606828
AbstractPlus | Full Text: PDF(592 KB) IEEE CNF
Rights and Permissions

15. **Finite state machine extraction from hardware description languages**
Giomi, J.-C.;
ASIC Conference and Exhibit, 1995., Proceedings of the Eighth Annual IEEE International
18-22 Sept. 1995 Page(s):353 - 357
Digital Object Identifier 10.1109/ASIC.1995.580747
AbstractPlus | Full Text: PDF(488 KB) IEEE CNF
Rights and Permissions

16. **Describing controlling hardware in VHDL**
Spillane, J.; Navabi, Z.;
ASIC Conference and Exhibit, 1991. Proceedings., Fourth Annual IEEE International
23-27 Sept. 1991 Page(s):P8 - 1/1-4
Digital Object Identifier 10.1109/ASIC.1991.242933
AbstractPlus | Full Text: PDF(260 KB) IEEE CNF
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17. **PLD implements cache control for a 33-MHz 80486**
Legenhausen, J.;
ASIC Conference and Exhibit, 1991. Proceedings., Fourth Annual IEEE International
23-27 Sept. 1991 Page(s):P16 - 2/1-4
Digital Object Identifier 10.1109/ASIC.1991.242883
AbstractPlus | Full Text: PDF(308 KB) IEEE CNF
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18. **Fault modeling and testing of self-timed circuits**
Guillory, S.S.; Saab, D.G.; Yang, A.;
VLSI Test Symposium, 1991 'Chip-to-System Test Concerns for the 90's'. Digest of Papers
15-17 April 1991 Page(s):62 - 66
Digital Object Identifier 10.1109/VTEST.1991.208134
AbstractPlus | Full Text: PDF(412 KB) IEEE CNF
Rights and Permissions

19. **Matrix computations in arrays of DSPs**
Moreno, J.; Medina, M.;
Application Specific Array Processors, 1992. Proceedings of the International Conference on
4-7 Aug. 1992 Page(s):496 - 510
Digital Object Identifier 10.1109/ASAP.1992.218549
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